## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: A Novel BISR Mode to Test the Redundant Elements and Regular Functional Memory to Avoid Test Escapes )  Inventor: Ghasi R. Agrawal et al. )  Art Unit: 2117 )  Examiner: Steve N. Nguyen )	Serial No.:	10/701,332	)	•		
the Redundant Elements and Regular Functional Memory to Avoid Test Escapes )  Inventor: Ghasi R. Agrawal et al. )  Art Unit: 2117 )  Examiner: Steve N. Nguyen )	Filed:	November 4, 2003	)	Conf. No.:	5874	
Art Unit: 2117 ) Examiner: Steve N. Nguyen )	Title:	the Redundant Elements and Regular Functional Memory	)			
Examiner: Steve N. Nguyen )	Inventor:	Ghasi R. Agrawal et al.	)			
ý)	Art Unit:	2117	)			
Atty. Ref: 03-1343 )	Examiner:	Steve N. Nguyen	)			
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## RESPONSE TO THE OFFICE ACTION MAILED JULY 17, 2008

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In the matter of the above-identified application and in response to the Office Action mailed July 17, 2008, kindly enter the following amendments and consider the following remarks toward reconsideration of the present application.